Silicon Creations

LOW POWER MOBILE SoC

ON-CHIP CLOCK DISTRIBUTION

INTERNET OF THINGS

AUTOMOTIVE

DDR/LPDDR
PCIe

LC-PLL & RING-PLL
up to 18GHz

MULTI-PROTOCOL
SERDES
32Gbps and more
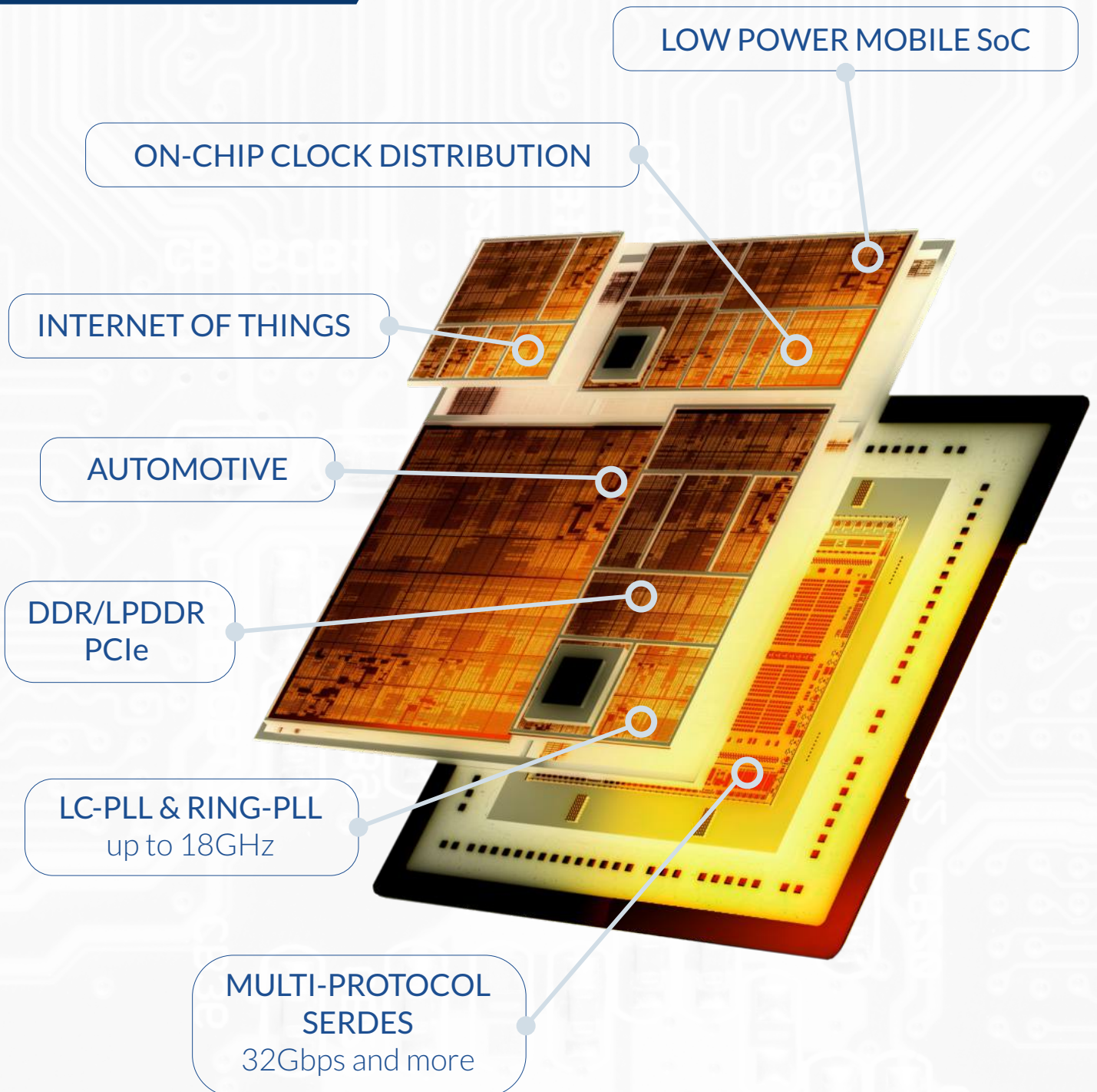
Majority of World's
TOP 50 INTEGRATED CIRCUITS
companies work with us!

Microelectronics world is here!

FinFET

PLANAR

down to

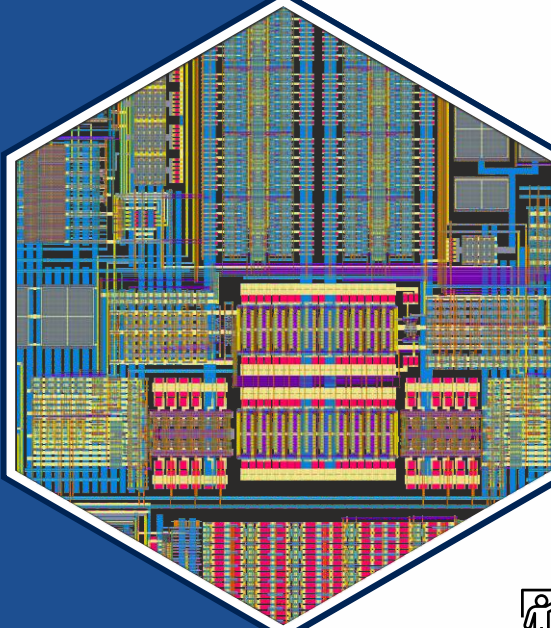## CMOS 3nm

FD-SOI

GAA

Hardware Verification & Validation Engineer

- multidisciplinary, hardware-engineering tasks
- high-speed digital design
- high-end laboratory equipment
- PCB & Systems design
    (Altium Designer / Keysight ADS)
- Signal Integrity, Power Integrity
- debugging & analyses
- MATLAB/Python/C++/Git/SCPI
- mechanical design
- test reports

Analog & Mixed-Signal Layout Design Engineer

- developing layout of integrated circuits
- post-layout simulation & verification
- product-related documentation
- product development & maintenance
- customer support
- Silvaco Expert/Cadence Virtuoso

Mixed-Signal Design Engineer

- simulation of the mixed-signal circuits
- test plans and testbenches
- analysis & visualization of simulation results
- SPICE, scripting, Python
- Silvaco Gateway

Mixed-Signal Verification Engineer

- generation & verification of Verilog behavioral models
- design & verification of RTL blocks
- scripting infrastructure & verification metholodgies
- supporting industry-leading customers
- Verilog/SystemVerilog
- Perl/Python/Bash

IP Library Characterization Engineer

- development of timing model libraries of PLL/SerDes/LVDS products
- development of internal characterization flow
- design of verification methodologies
- customer support
- analysis & visualization
- SPICE
- Python/Perl/Bash/TCL

Automotive Functional Safety Engineer

- ISO26262
- reliability analysis
- FMEDA, FTA, FIT
- safety manual & documentation
- Perl/Matlab/Verilog/Python

tsmc

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hrpl@siliconcr.com